METHOD AND APPARATUS FOR VLSI CLOCK GATED POWER ESTIMATION USING LCB COUNTS

ABSTRACT

5   A method, an apparatus, and a computer program are provided for modeling algorithm for power consumption with improved accuracy. The improved model allows for variable operation of Local Clock Buffers (LCBs), which consume significant amounts power. Also, the amount of power

10   consumed by each LCB can be varied. These variations of operation of LCBs and power consumption by LCBs allows for a more realistic model of operation of a given circuit or macro instead of the traditional "worst-case scenario" of power consumption.